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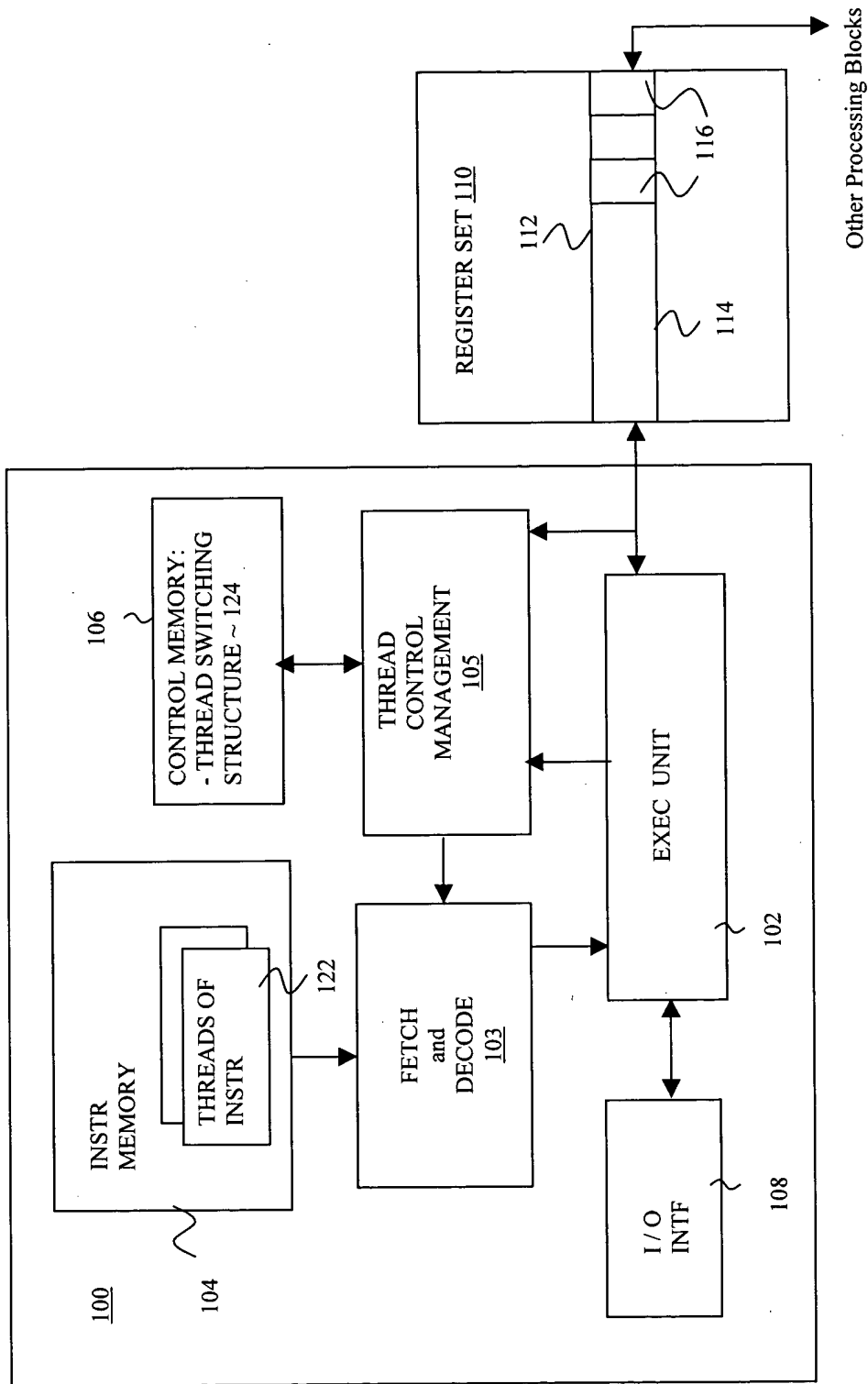


Fig. 1

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thread0: -- primary thread - thread0 crth thread1, GR8, -QPN0; 202 ~ crth thread2, GR10, -QPN1; crth thread3, GR11, -QPW0; crth thread4, GR12, -QPW1; 204 ~ kill;	122a
thread1: pack GR8, GR9, QPN0; 206 ~ switch;	122b
thread2: shr GR10, #sftcnt, QPN1; switch;	122c
thread3: mpy GR11, #scale, LR1; shr LR1, sftcnt, QPW0; switch;	122d
thread4: add offset, GR12, QPW1; switch;	122e

Fig. 2

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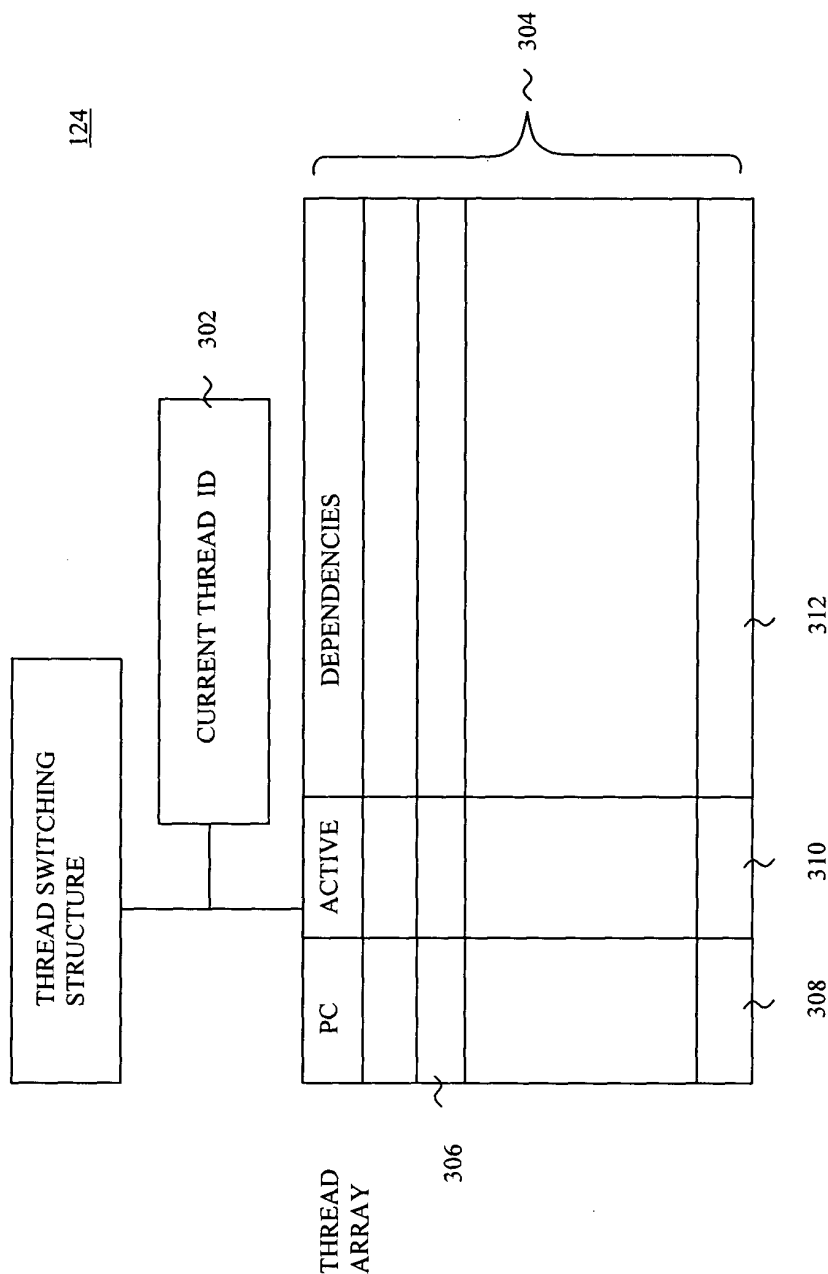


Fig. 3

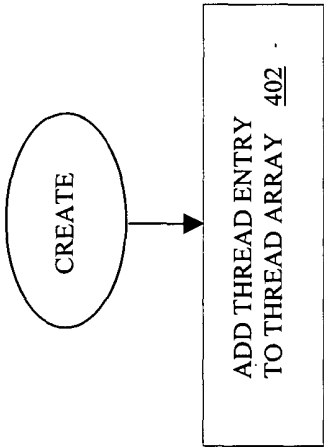


Fig. 4a

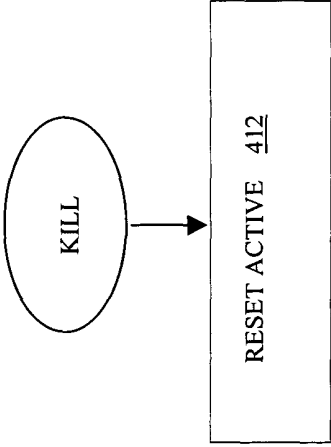


Fig. 4b

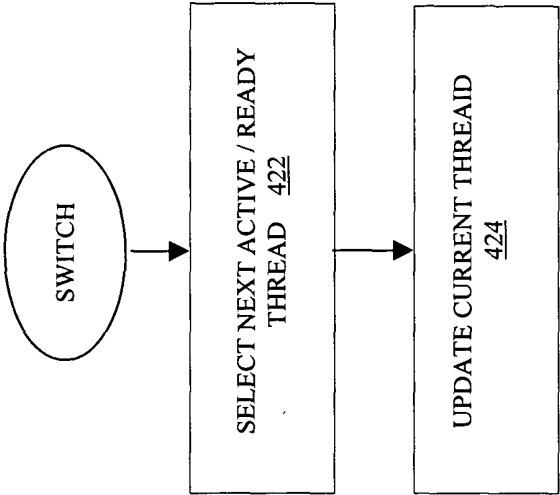


Fig. 4c

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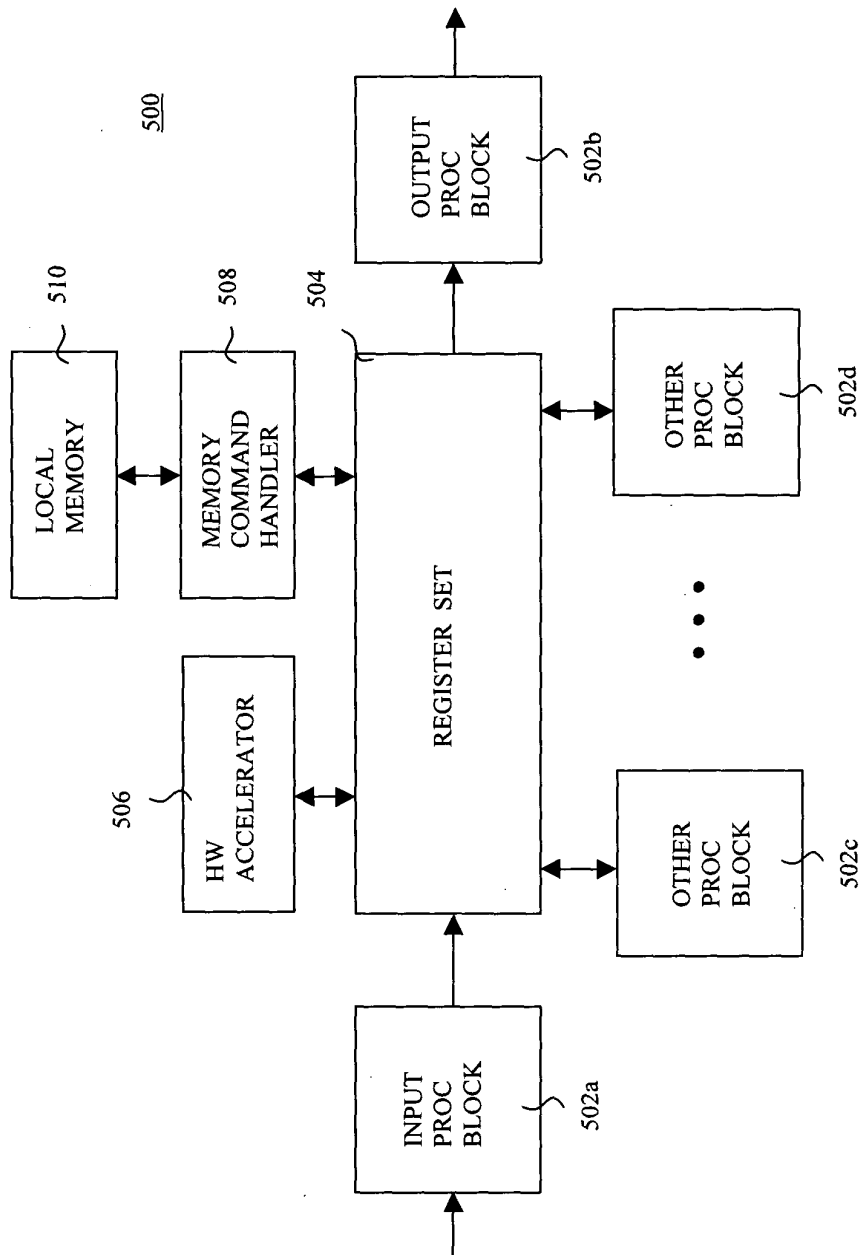


Fig. 5

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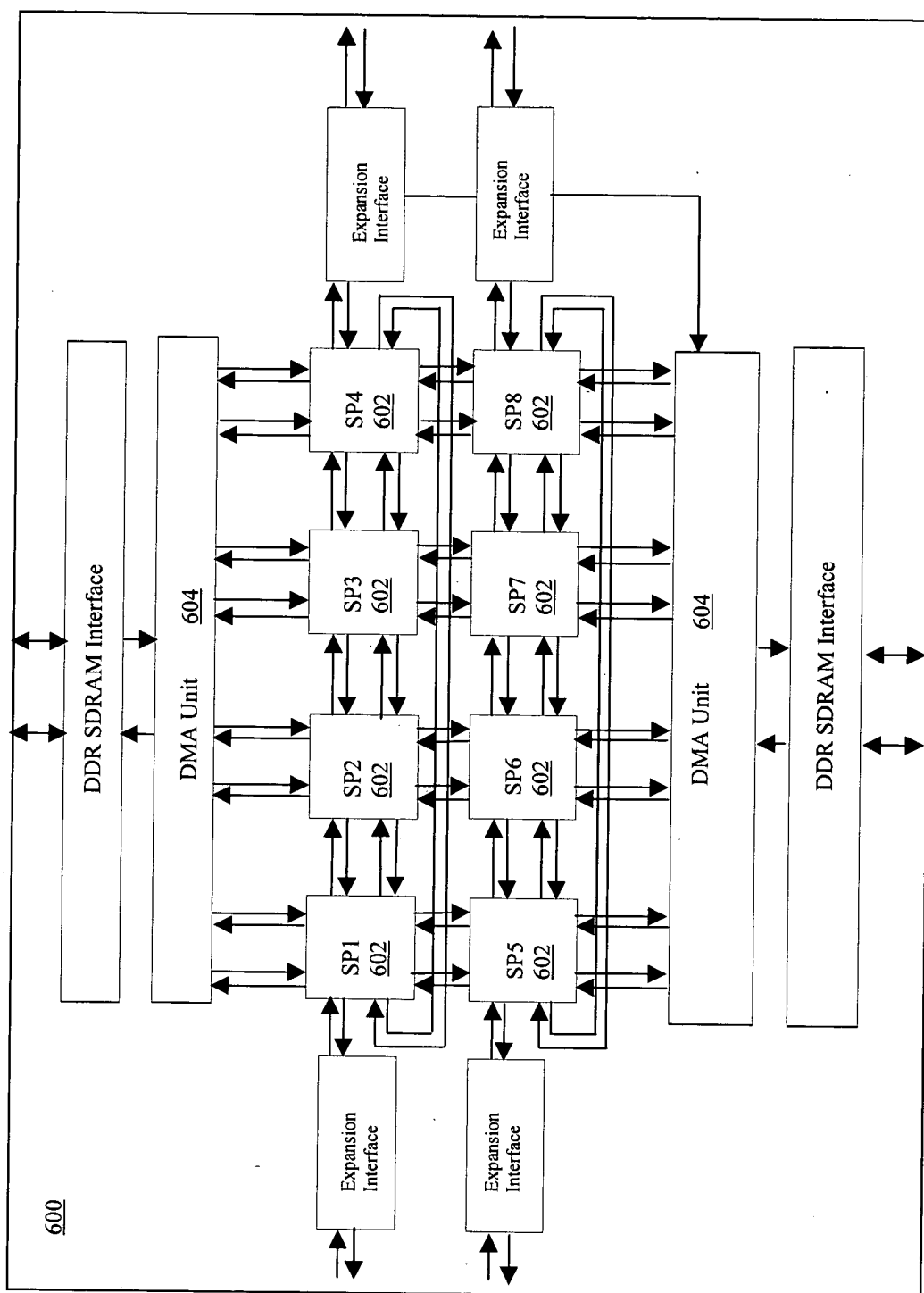


Fig. 6

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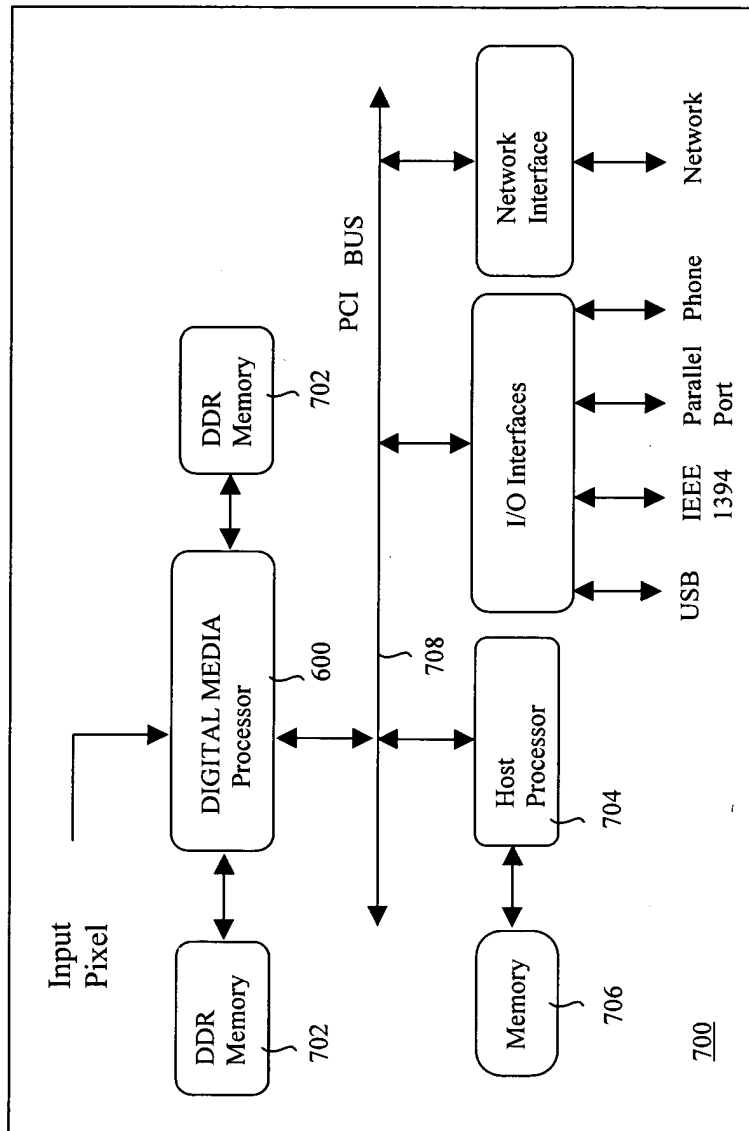


Fig. 7